

ABSTRACT OF THE DISCLOSURE

Disclosed is a system, which divides a memory into a plurality of equally-sized sub-memories and controls
5 an address of each sub-memory, thereby significantly increasing the access speed to an auxiliary memory unit, which comprises a SCSI (Small Computer System Interface) interface controller for converting a SCSI interface bus into a PCI (Peripheral Component Interconnect Bus)
10 interface bus for use in the system, a memory card module for storing data on the PCI interface bus therein, the memory card module being divided into a plurality of equally-sized memory blocks, and a CPU (Central Processing Unit) module for processing
15 writing data on the PCI interface bus in the memory card module and reading out the data therefrom. The memory card module includes a PCI to memory controller of a tree hierarchical configuration, which is disposed between the PCI interface bus and the plurality of sub-
20 memories as a bridge, for controlling access to the plurality of sub-memories, which is distributed in a hierarchical fashion.